University of Massachusetts Amherst

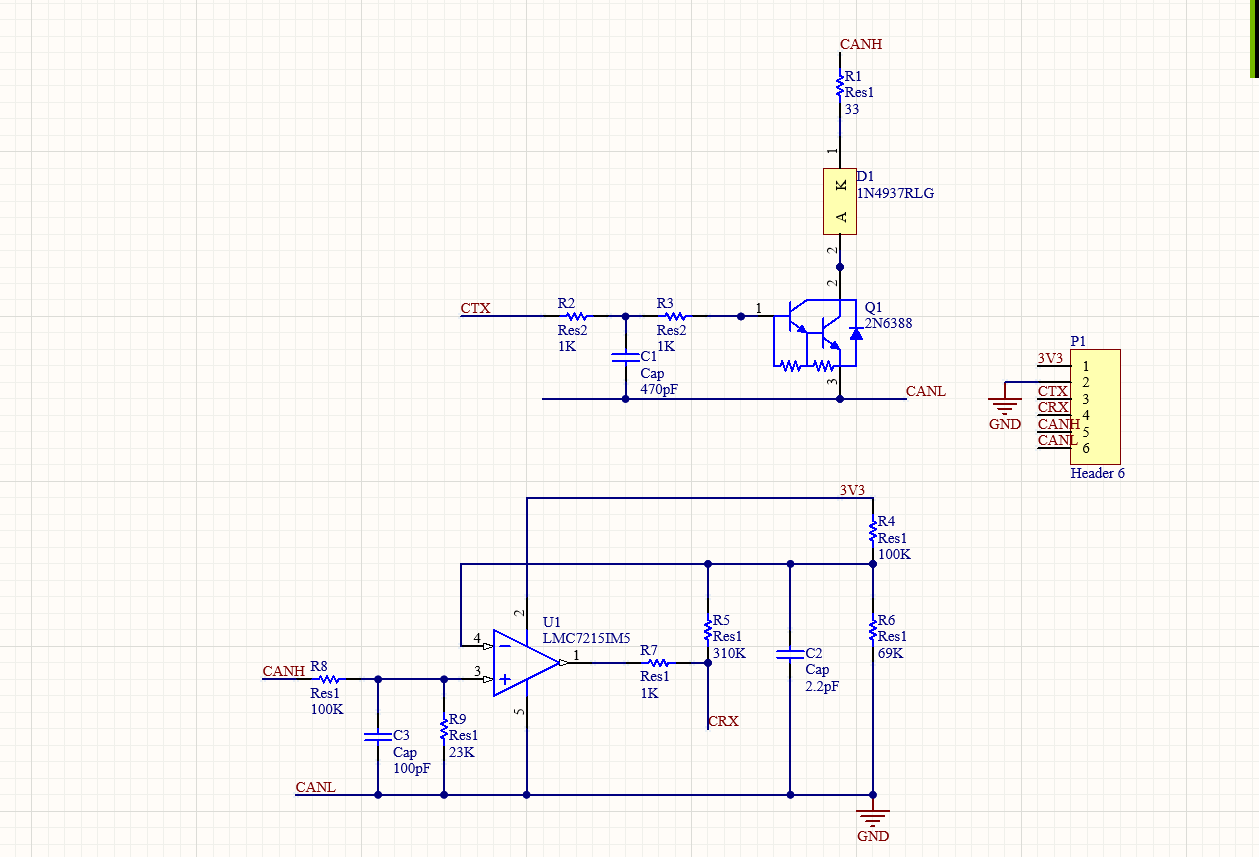
College of Engineering

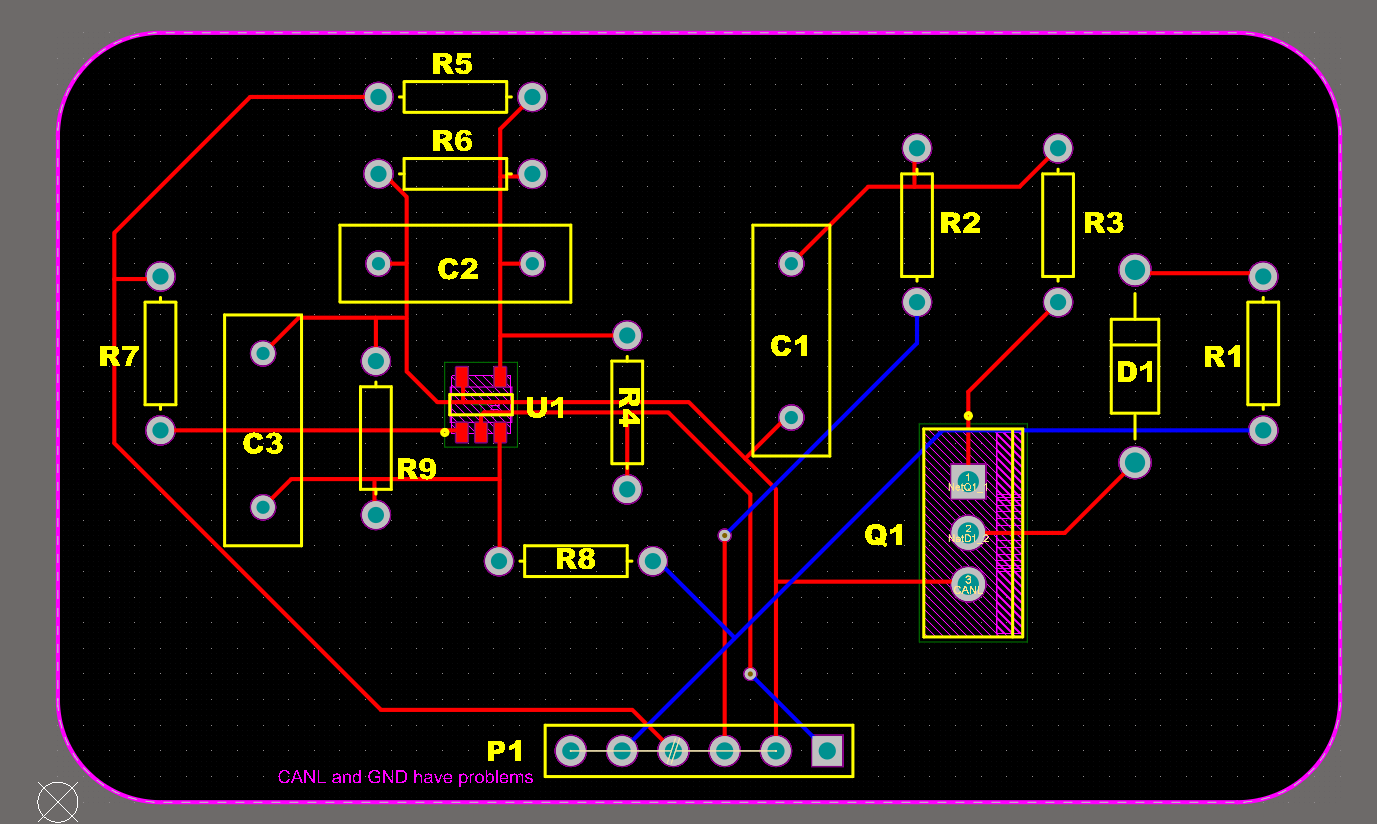
Department of Electrical and Computer Engineering

ECE 416 - Senior Design Project II

Spring 2021

SDP21 Progress Report

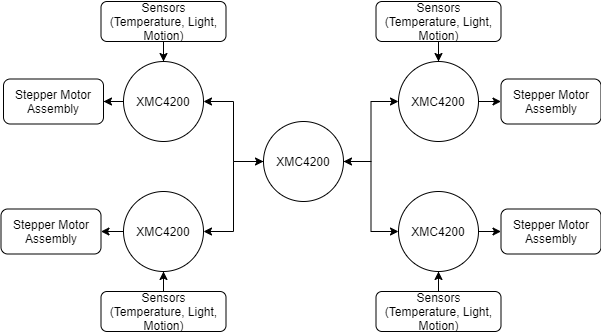




Problems

There is a minor error in the schematic that needs to be fixed before I can finish the PCB design and send to manufacturing.

The P1 component shows an error because CANL and GND are not connected. I recall last semester that the system needed to have them separated, is that still the case? I am trying to understand the consequences of making the board this way.



To display the tree topology, we are going to have inputs and outputs wired in this manner to show that a signal will be sent correctly across the system.